

--13. (Amended) The data recording apparatus as set forth in claim 10,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in binary coded decimal notation, and

wherein in the second addressing system, each digit of hour, minute, second, and frame of an address is represented in binary coded decimal notation.

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--14. (Amended) The data recording apparatus as set forth in claim 10,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in binary coded decimal notation, and

wherein in the second addressing system, addresses are represented in binary notation.

REMARKS

Claims 1-16 remain in the application with claims 5-8, 10, 13, and 14 having been amended hereby.

As will be noted from the Declaration, Applicants are

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citizens and residents of Japan and this application originated there.

Accordingly, the amendments made to the specification are provided to place the application in idiomatic English, and the claims are amended to place them in better condition for examination.

An early and favorable examination on the merits is earnestly solicited.

Respectfully submitted,  
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VERSION WITH MARKINGS TO SHOW CHANGES MADE  
IN THE ABSTRACT OF THE DISCLOSURE

Please amend the Abstract by rewriting same to read as follows.

Data in the same signal format as a conventional CD is recorded to a first part [PA1. Compressed] of a record medium and compressed digital data in CD-ROM format in double density is recorded to a second part [PA2] of the record medium. Addresses of a first session including the first part are represented in the notation of minute, second, and frame[. Addresses], and addresses of a second session including the second part are represented in binary notation. When the record position is moved from the first session to the second session, [the] an end address of the first session is converted into an address of the second session [(at step S3)]. [At step S4, designating the] The start address [as address B,] is then designated and second addresses (for example, addresses represented in binary notation) are generated. When there is a non-record portion between the two sessions, an address value corresponding to the non-record portion is added

to the start address. The resultant address is used as the new start address.

IN THE CLAIMS

Please amend claims 5-8, 10, 13, and 14 by rewriting same to read as follows.

--5. (Amended) The data record medium as set forth in claim 1,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in [BCD] binary coded decimal notation, and

wherein in the second addressing system, each digit of hour, minute, second, and frame of an address is represented in [BCD] binary coded decimal notation.

--6. (Amended) The data record medium as set forth in claim 1,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in [BCD] binary coded decimal notation, and

wherein in the second addressing system, addresses are represented in binary notation.

--7. (Amended) The data record medium as set forth in claim 1,

wherein each of said first record area and said second record area is a program area surrounded respectively by a lead-in area and a lead-out area.

--8. (Amended) The data record medium as set forth in claim 1,

wherein [the] a record density of said first record area is different from [the] a record density of said second record area.

--10. (Amended) A data recording apparatus for recoding data on a data record medium [whose] having a record area [is] divided into at least a first record area and a second record area, comprising:

recording means for recording first data to the first record area in a first addressing system and for recording second data to the second record area in a second addressing system; and

controlling means for causing addresses to be recorded non-redundantly in the first record area and the second record area when one of the first addressing system and the second addressing system is converted into the other addressing system.

--13. (Amended) The data recording apparatus as set forth in claim 10,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in [BCD] binary coded decimal notation, and

wherein in the second addressing system, each digit of hour, minute, second, and frame of an address is represented in [BCD] binary coded decimal notation.

--14. (Amended) The data recording apparatus as set forth in claim 10,

wherein in the first addressing system, each digit of minute, second, and frame of an address is represented in [BCD] binary coded decimal notation, and

wherein in the second addressing system, addresses are represented in binary notation.